CLAIM AMENDMENTS:

Please amend the claims as follows:

1. (Currently amended) A semiconductor integrated circuit device comprising:

a power wiring ~~whose one end is~~ having a first terminal connected to a first power supply of a first polarity;

a ground wiring ~~whose one end is~~ distinct from said power wiring, and having a first terminal connected to a ground; and

a plurality of circuits connected in parallel between the power wiring and the ground wiring,

wherein ~~the other end of~~ the ground wiring has a second terminal [[is]] connected to a current generating section for generating a predetermined current in said ground wiring in a state in which the current generating section is connected to a ~~negative~~ second power supply of a second polarity opposite to said first polarity.

2. (Currently amended) A semiconductor integrated circuit device comprising:

a power wiring ~~whose one end is~~ having a first terminal connected to a first power supply of a first polarity;

a ground wiring ~~whose one end is~~ distinct from said power wiring, having a first terminal connected to a ground and having a second terminal;

a plurality of circuits connected in parallel between the power wiring and the ground wiring; and

a current generating section ~~whose one end is~~ having a first terminal connected to the ~~other end~~ second terminal of the ground wiring to generate a predetermined current in said ground wiring in a state in which ~~the other end~~ a second terminal of the current generating section is connected to a ~~negative~~ second power supply of a second polarity opposite to said first polarity.

3. (Currently amended) A semiconductor integrated circuit device comprising:

a power wiring ~~whose one end is~~ having a first terminal connected to a first power supply of a first polarity;

a ground wiring distinct from said power wiring, ~~whose one end is~~ and having a first terminal connected to a ground;

a plurality of circuits connected in parallel between the power wiring and the ground wiring;

a ~~negative~~ second power supply of a second polarity opposite to said first polarity; and

a current generating section ~~whose one end is~~ having a first terminal connected to the ground wiring and ~~whose other end is~~ having a second terminal

connected to the ~~negative~~ second power supply to generate a predetermined current in said ground wiring .

4. (Currently amended) The semiconductor integrated circuit device according to claim 1, wherein the second terminal of said ground wiring connected to said current generating section is disposed in a ~~wiring~~ second portion of said ground wiring most distant from a first portion of said ground wiring in which a ground potential is supplied to the ground wiring via said first terminal of said ground wiring.

5. (Currently amended) The semiconductor integrated circuit device according to claim 1, wherein the current generating section is either one of a current source [[and]] or an operating circuit which consumes a predetermined current to operate.

6. (Original) The semiconductor integrated circuit device according to claim 5, wherein the operating circuit which consumes the predetermined current to operate is a clock generator which outputs a clock signal.

7. (Original) The semiconductor integrated circuit device according to claim 6, wherein the clock generator is connected to a level shifter for converting a

level of the outputted clock signal to supply the clock signal to the plurality of circuits.